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18351 U.S. PTO

22388 U.S. PTO
10/657584
09/08/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of: Richard W. Ollila et al.

Title: TEST UNIT AND ENCLOSURE FOR TESTING INTEGRATED CIRCUITS

Attorney Docket No.: 884.295US2

Customer No.: 21186

PATENT APPLICATION TRANSMITTAL

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ **DIVISIONAL** of prior Patent Application No.(09/706952) (under 37 CFR 1.53(b)) comprising:
 - ☒ Specification (18 pgs, including claims numbered 1 through 29 and a 1 page Abstract).
 - ☒ Formal Drawing(s) (3 sheets).
 - ☒ Copy of signed Combined Declaration and Power of Attorney (3 pgs) from prior application.
 - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
 - ☒ Charge Deposit Account No. 19-0743 in the amount of \$750.00 to pay the filing fee.
- ☒ Prior application is assigned of record to Intel Corporation.
- ☒ Information Disclosure Statement (1 pgs), Form 1449 (1 pgs)
References NOT enclosed, cited in prior application.
- ☒ Preliminary Amendment (5 pgs).

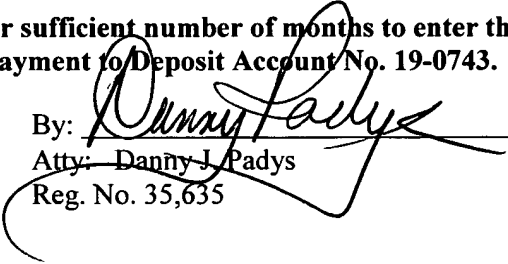
The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	13-20	0	x 18.00 =	\$0.00
INDEPENDENT CLAIMS	2-3	0	x 84.00 =	\$0.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$750.00
TOTAL				\$750.00

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Date of Deposit: September 8, 2003

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.